IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No	10/678 513
Filing Date	October 3, 2003
Inventor	
Assignee	Micron Technology, Inc.
Group Art Unit	
Examiner	
Attorney's Docket No	
Title: Semiconductor Processing Methods	of Forming a Contact Opening to a
Conductive Line and Methods o	f Forming Substrate Active Area
Source/Drain Regions	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

This Supplemental Information Disclosure Statement is being filed before the mailing date of a first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Bv

D. Brent Kenady Reg. No. 40,045